**TABLE OF CONTENTS**

**Page**

**TITLE PAGE** i

**APPROVAL SHEET** ii

**ABSTRACT** iii

**DEDICATION** iv

**ACKNOWLEDGEMENT** v

**TABLE OF CONTENTS** vi

**LIST OF FIGURES** xi

**LIST OF TABLES** xii

**CHAPTER 1: INTRODUCTION**  1

1.1 Background of the Study 1

1.2 Statement of the Problem 2

1.3 Objectives of the Study 2

1.4 Significance of the Study 3

1.5 Scope and Limitations 3

1.6 Definition of Terms 3

1.7 Theoretical Framework 7

1.7.1 Hardware 7

1.7.1.1 SoCKit Development Board 7

1.7.1.1.1 Cyclone V SoC 8

1.7.1.1.1.1 Dual-Core ARM Cortex-A9 10  
 MPCore Processor

1.7.1.1.2 USB 3300 Hi-Speed USB Host, 11  
 Device or OTG PHY

1.7.1.1.3 KSZ9021RL/RN Gigabit Ethernet 12  
 Transceiver

1.7.1.1.4 VGA 13

1.7.1.1.5 Memory 14

1.7.1.2 Logitech C525 Webcam 14

1.7.1.3 Micro SD Card 14

1.7.2 Software 15

1.7.2.1 In-System 15

1.7.2.1.1 Linux Kernel 15

1.7.2.1.2 Debian Operating System 15

1.7.2.1.3 OpenCV Library 16

1.7.2.1.4 Lightweight X11 Desktop 16  
 Environment (LXDE)

1.7.2.2 Development Software 17

1.7.2.2.1 Altera Quartus 17

1.7.2.2.2 Qsys - Altera’s System Integration Tool 17

1.7.2.2.3 SoC Embedded Design Suite 17

1.7.2.2.4 Linaro Toolchain 18

1.7.2.2.4.1 Linaro GCC 18

1.7.2.2.5 Github 18

1.7.2.2.6 Win32 Disk Imager 19

1.7.2.2.7 PuTTY 19

1.7.2.2.8 Others 19

**CHAPTER II: Review of Related Literature**  21

2.1 OpenCV 21

2.2.1 QpenCV Face Detection 21

2.2.2 OpenCV Face Recognition 21

2.2 Current State of OpenGV Acceleration. 22

2.2.1 OpenGV GPU 22

2.2.3 OpenCV IPP 23

2.2.4 OpenCV Applications with Zynq-7000 All 23  
 Programmable SoC

2.3 USB Video Class 24

**CHAPTER III: Methodology** 26

3.1 System Requirements 26

3.2 Top Level Face Detection System Design Overview 26

3.3 Hardware Preparation 27

3.4 Development Environment Preparation 28

3.5 Integration Layer 28

3.5.1 Golden Hardware Reference Design 28

3.6 OS Layer 29

3.6.1 Modified Linux Kernel Source 29

3.6.2 U-Boot Configuration 30

3.6.3 SD Card Image 30

3.6.3.1 The Debian Based Linaro Ubuntu Image 30

3.6.3.2 Partitioning of the SD Card Image 30

3.6.3.3 System Files 30

3.6.4 Utilities 31

3.6.5 OpenCV Library 31

**CHAPTER IV: Result and Discussions** 32

4.1 OpenCV Face Detection 32

4.2 System Bugs 32

4.2.1 USB UVC Bandwidth Issues with USB OTG 33

4.2.2 USB UVC has some issues FFMPEG 33

**CHAPTER V: Conclusion and Recommendation** 34

5.1 Conclusion 34

5.2 Recommendations 34

**BIBLIOGRAPHY**

**APPENDICES**

**Appendix A: Expanded Block Diagram of the Face Detection System** 35

**Appendix B: SoCKit Development Kits & Tools Detail** 36

**Appendix C:** **ARM® Cortex™-A9 MPCore™ Specification** 38

**Appendix D: FPGA Configuration Mode** 39

**Appendix E: Configuring U-Boot** 42

**Appendix F: From RocketBoards Org On How to Partition a uSD Card** 43

**Appendix G: Specs of the Intel CISC based system** 44

**Appendix H: Kernel Panic Logs** 45

**Appendix I: Researcher’s Notes on SoCKit Usage** 46

**Appendix J: Face Detection System SoCKit Test Results** 49

**Appendix K: Standard 512x512 pixels test image** 50

**Appendix L: Intel CISC Based Core i7 Test Results** 51

**LIST OF FIGURES**

**FIGURE PAGE**

**Figure 1 System Block Diagram of the SoCKit Evaluation Board**

**Figure 2 Cyclone V SoC Integrated Circuit Block Diagram**

**Figure 3 Cortex A9 MPCore**

**Figure 4 Basic ULPI USB Device Block Diagram**

**Figure 5 Connections between Cyclone V SOC and FPGA and Ethernet**

**Figure 6 Functional Block Diagram of the KSZ9021RL/RN**

**Figure 7 VGA Block Diagram**

**Figure 8 Connections between FPGA and DDR3**

**Figure 9 Top Level block diagram of the Entire System.**

**Figure 10 Top Level System Overview**

**Figure 11 Development Tools**

**Figure 12 Boot Process**

**LIST OF TABLES**

**TABLE PAGE**

**Table 1 Comparrison of CISC based and Cyclon V SoC.**